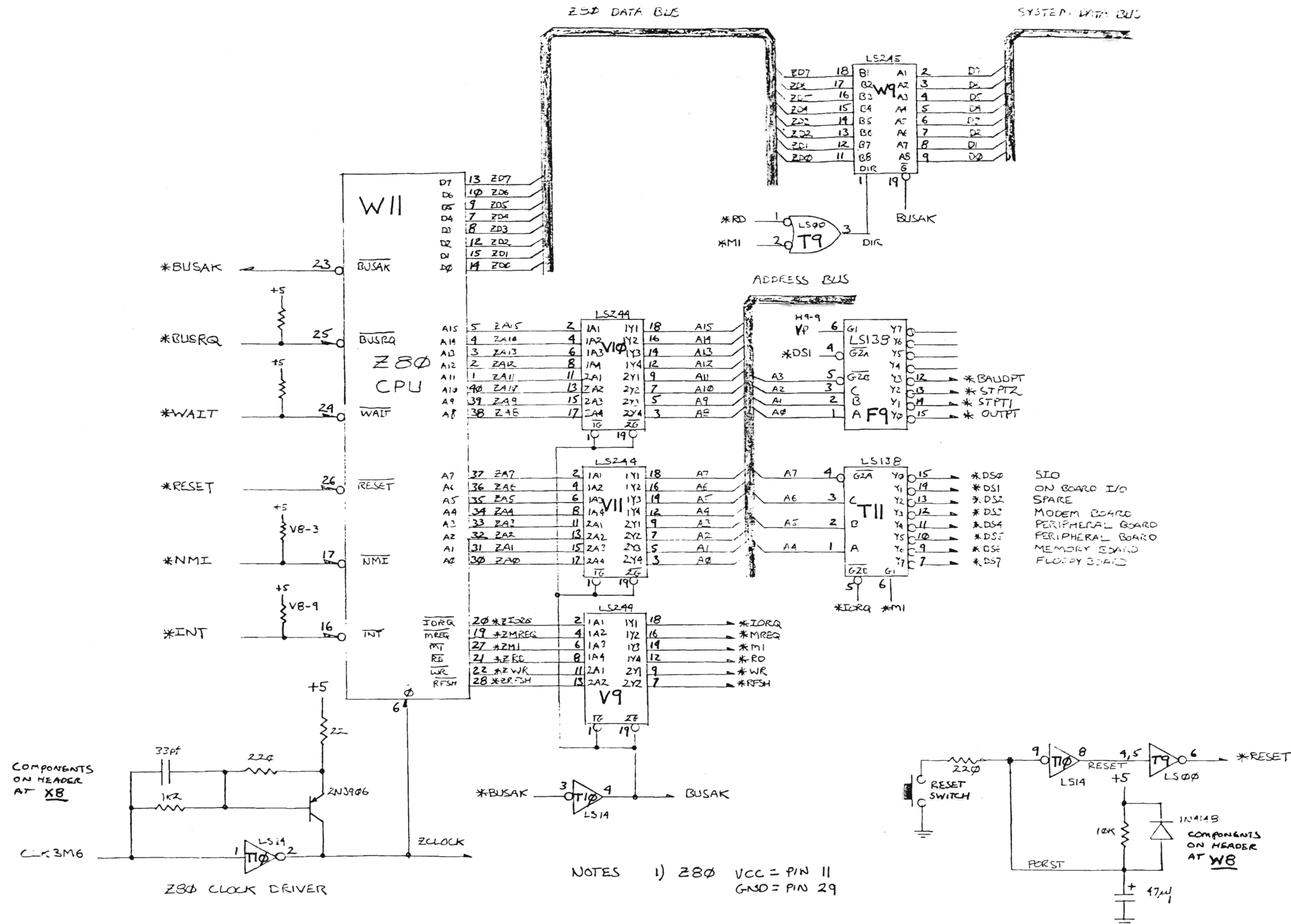


PROCESSOR SECTION

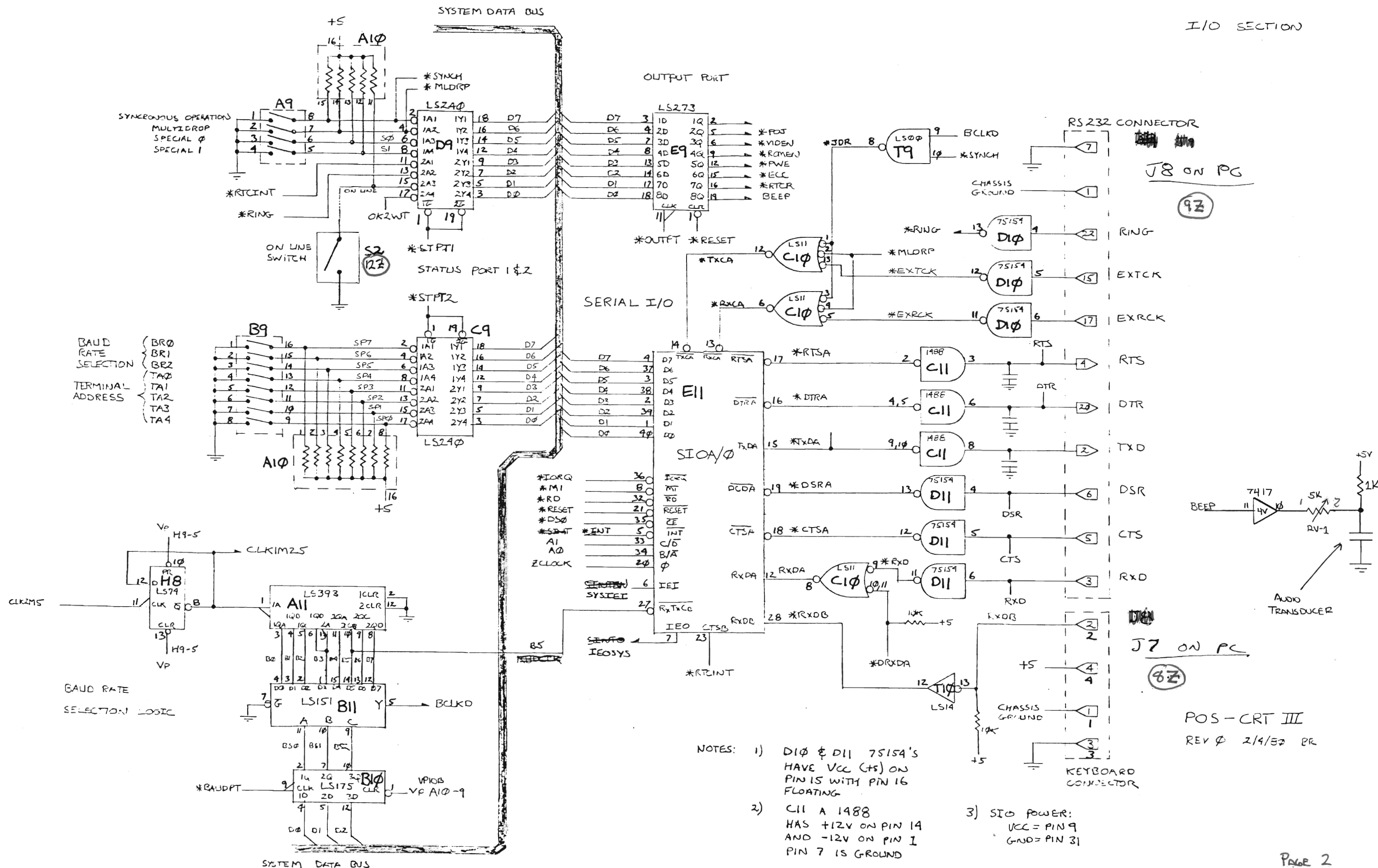


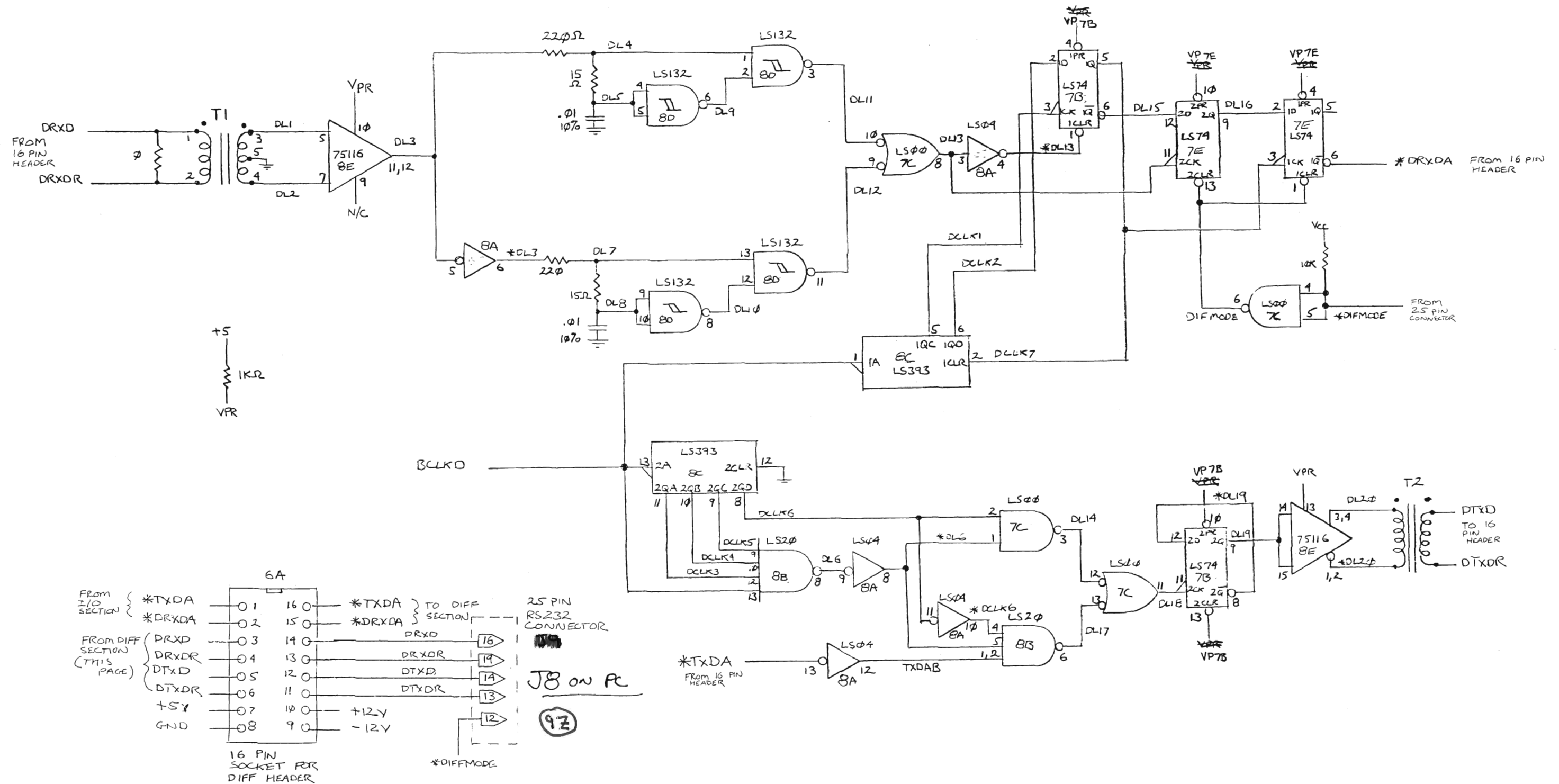
POS - CRT III

REV 0 2/01/80

BR

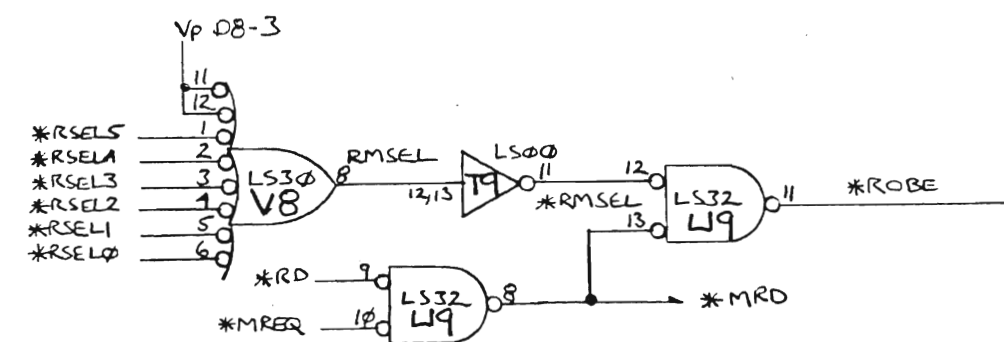
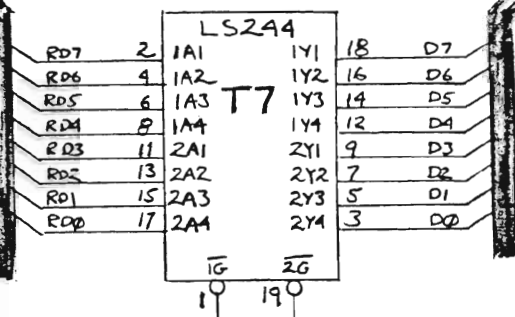
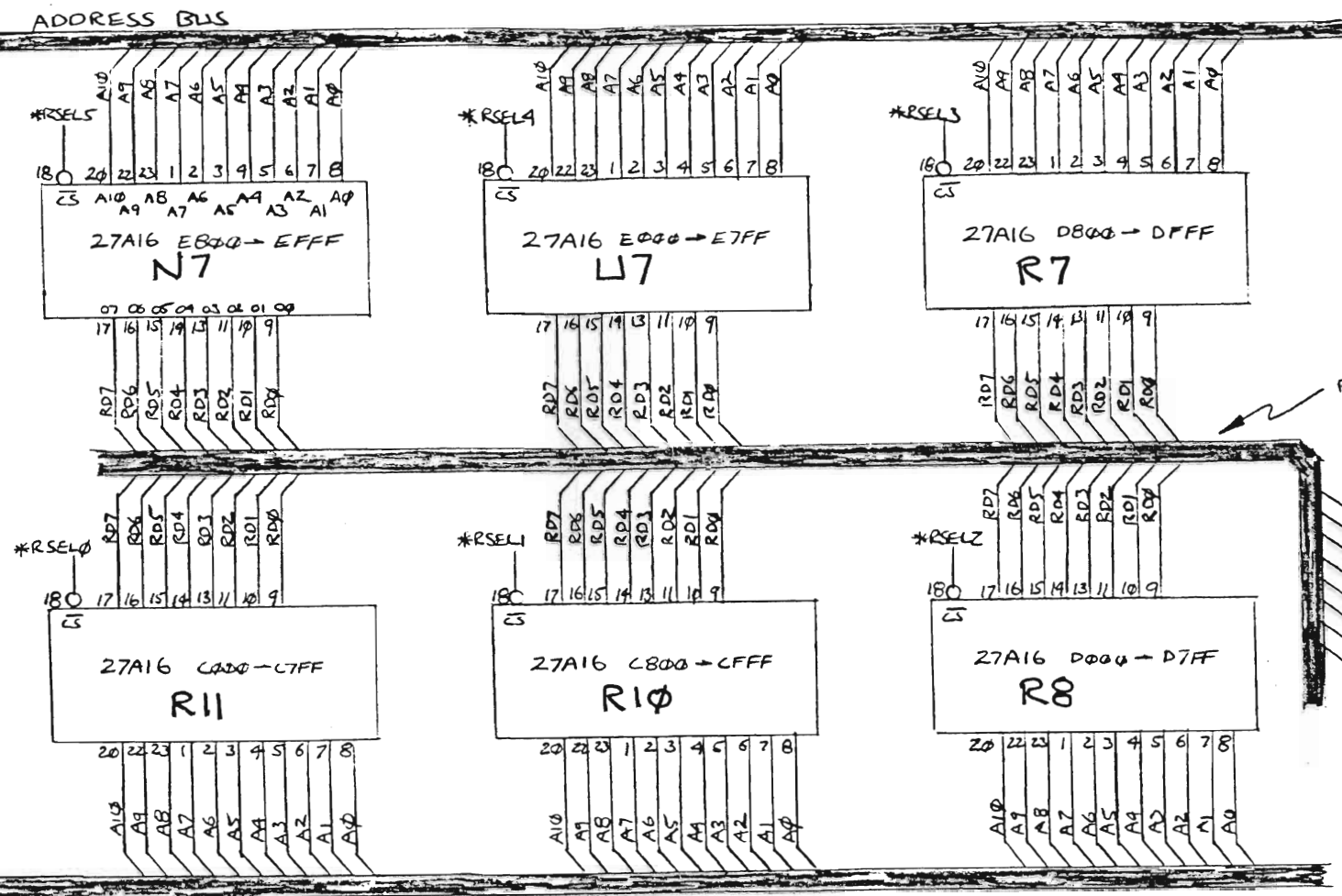
REV 1 3/17/84 ELIMINATE PDS
ALTER RESET





TRIAD CRT MODEL II

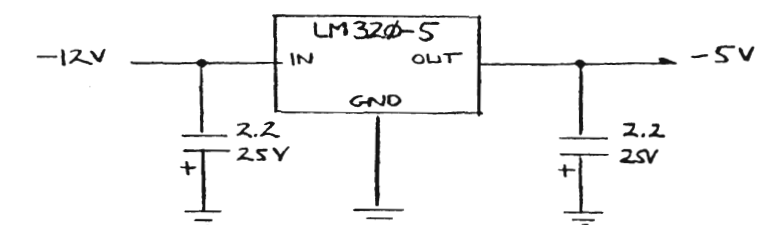
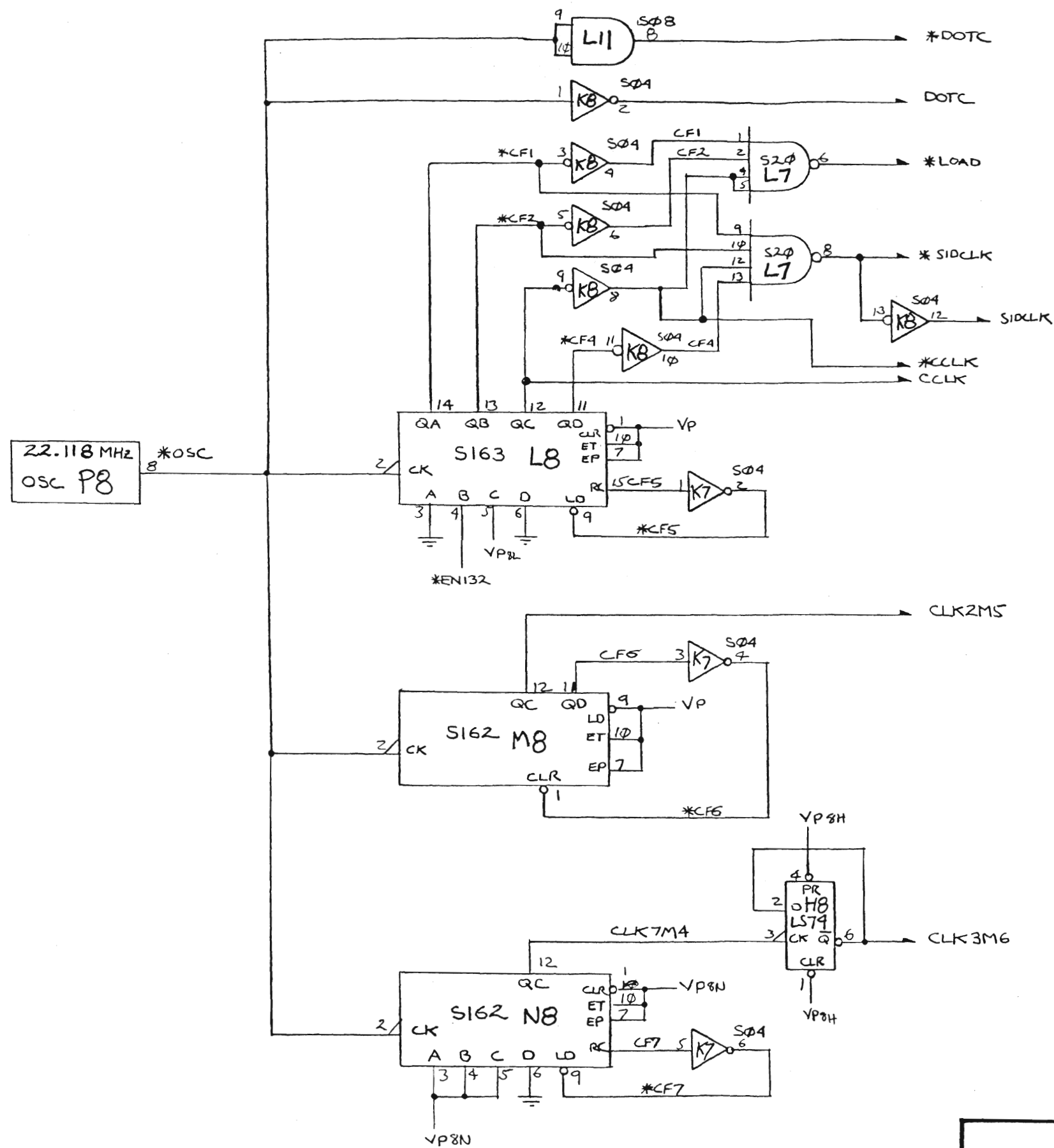
DRAWN 11/17/71 DR FROM 11/17/71	SIZE B	FSCM NO.	DWG. NO. DIFFERENTIAL SECTION	F
ISSUED	SCALE		SHEET	



POS-CRT III

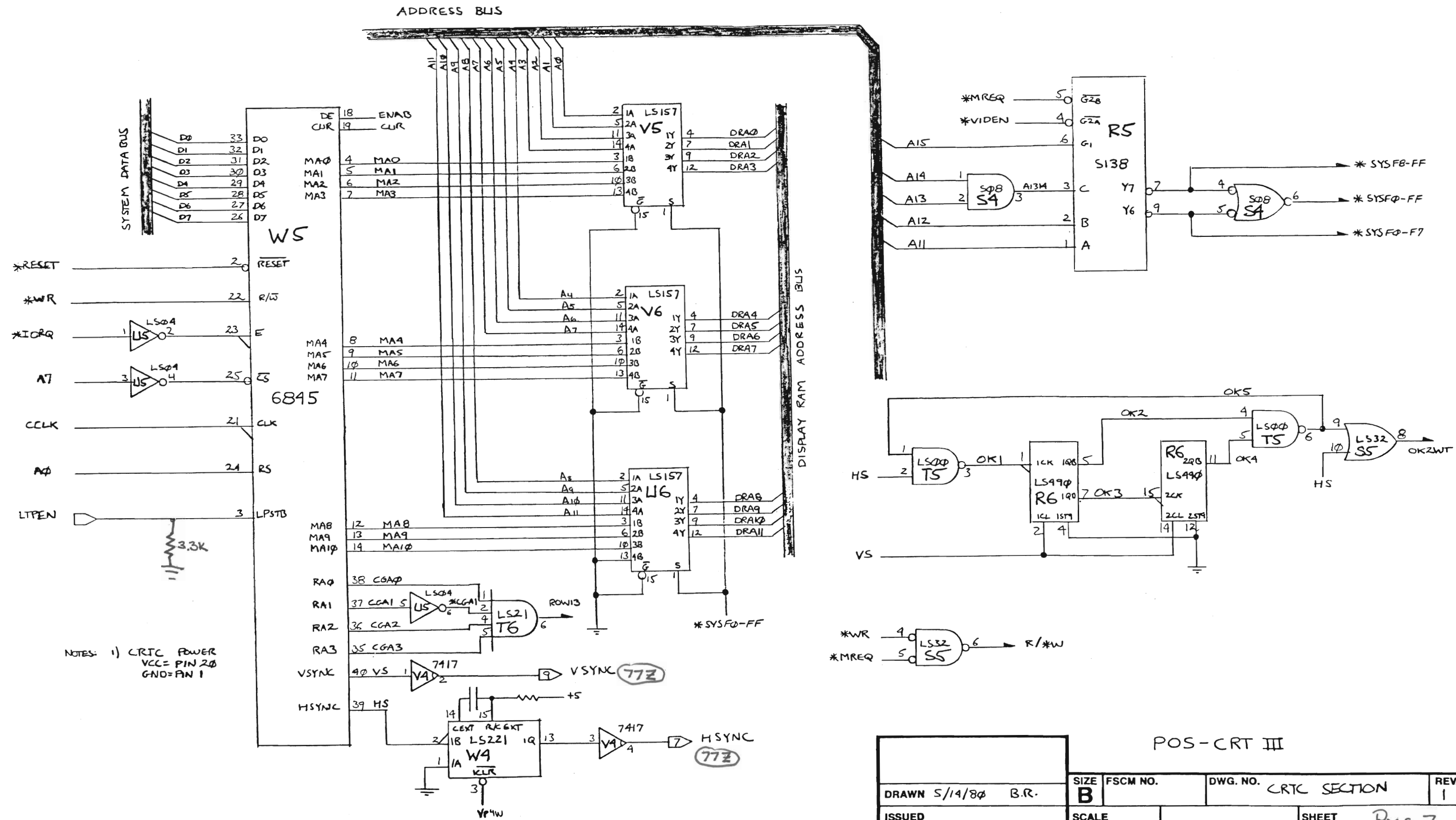
		POS - CRT III			
		SIZE B	FSCM NO.	DWG. NO.	ROM SECTION
DRAWN S-14-80 BR					
ISSUED		SCALE		SHEET	PAGE 4





POS - CRT III

DRAWN 3/27/80 D.R.	SIZE B	FSCM NO.	DWG. NO.
ISSUED	SCALE		CLOCK LOGIC
			SHEET



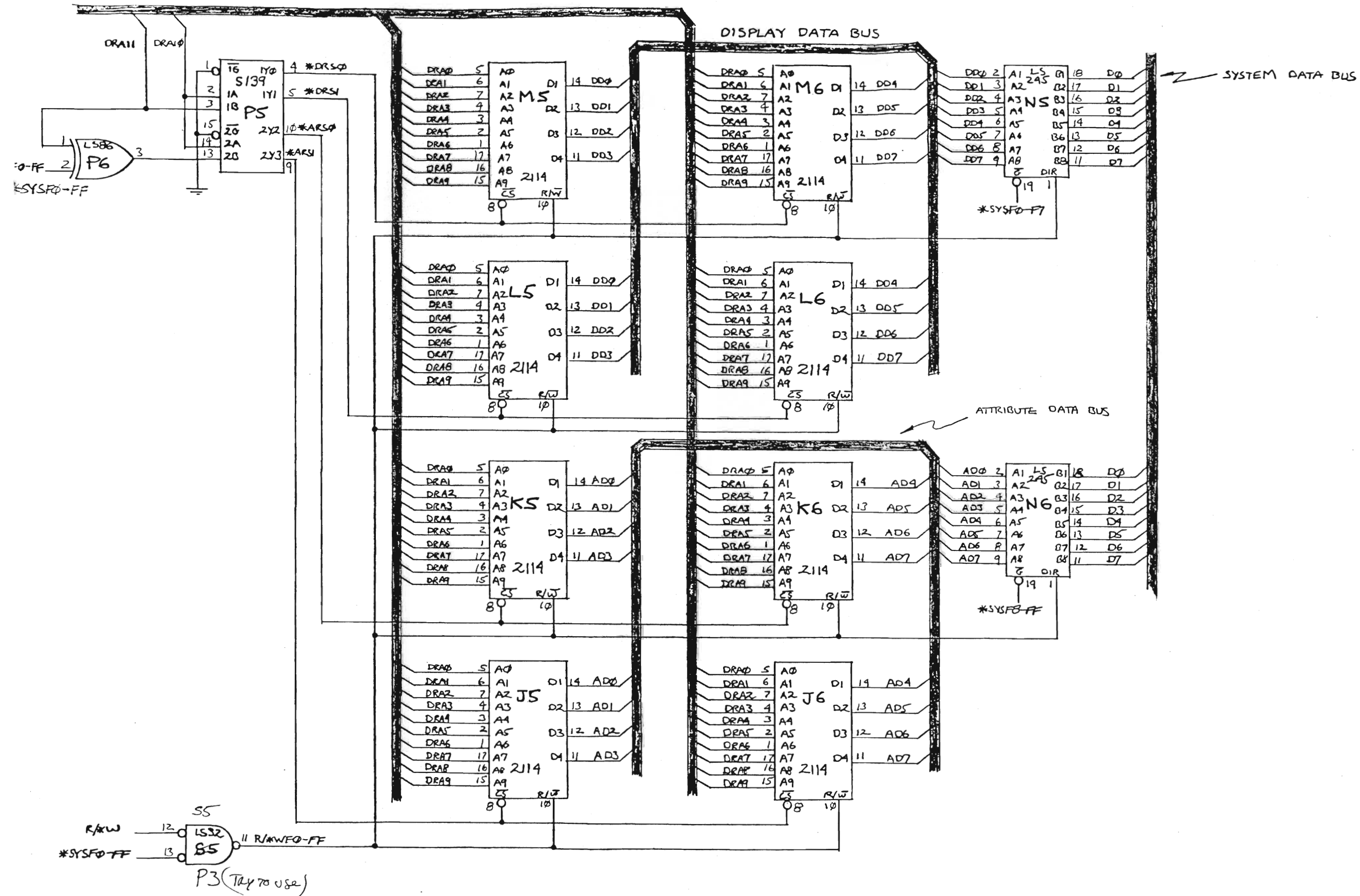
DISPLAY RAM ADDRESS BUS

DISPLAY DATA BUS

DISPLAY/ATTRIBUTE RAM

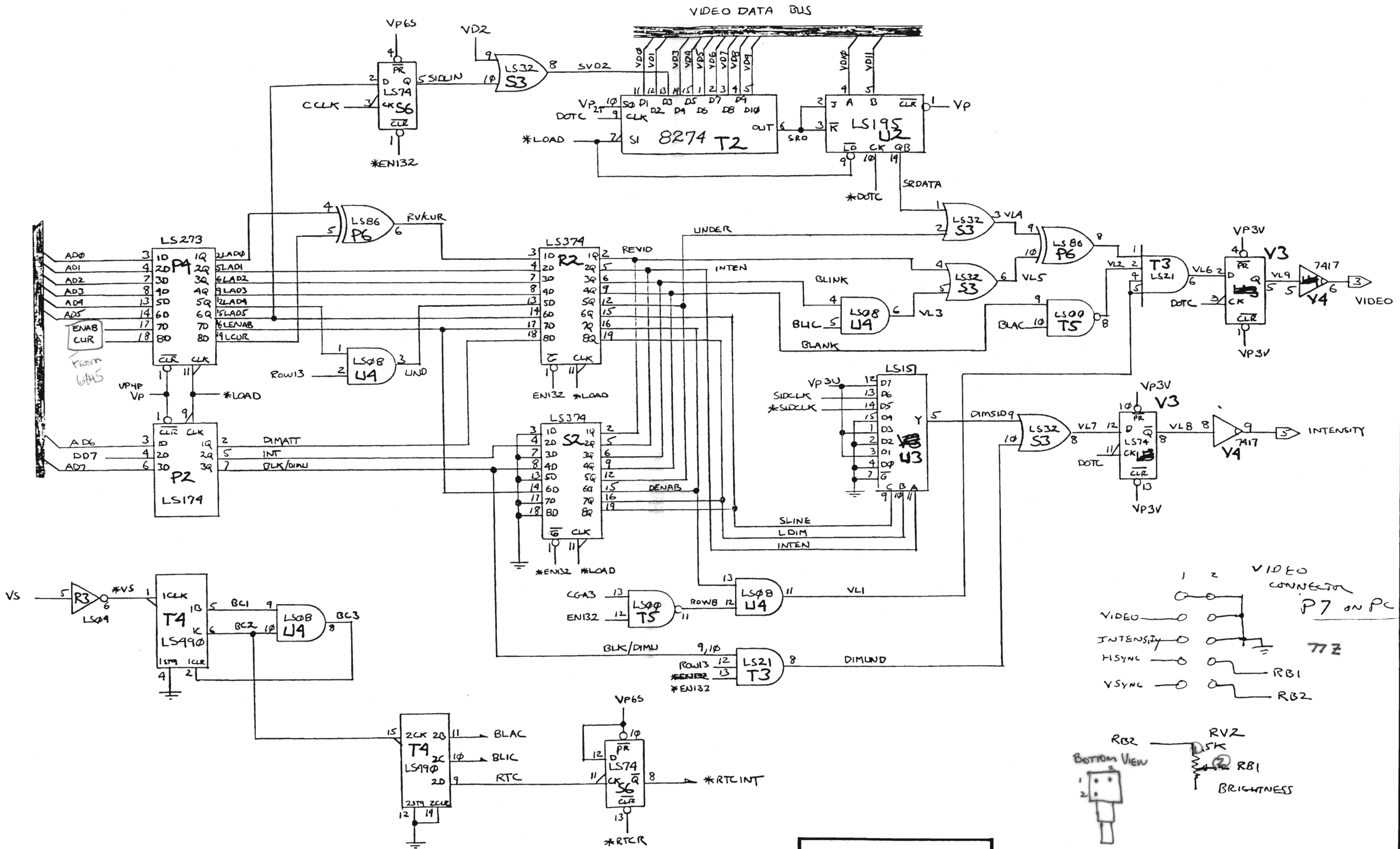
SYSTEM DATA BUS

ATTRIBUTE DATA BUS



POS - CRT III
REV 0 2/12/80

TT3/B



POS - CRT III

DRAWN 5/16/80 BR	SIZE B	FSCM NO.	DWG. NO. VIDEO SECTION	REV. 2
ISSUED	SCALE		SHEET	PAGE 10